Substitute Form PTO-1449 (Modified) U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 01997-254002	Application No. 09/377,372
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))	Applicant Arvind Mithal et al.	
	Filing Date August 19, 1999	Group Art Unit ~~2731~~ 2661

U.S. Patent Documents

Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation Yes	Translation No
MN	AB	*EP 0329233	10/02/89	European Patent Office				
MN	AC	*EP 0 82902120 A2	09/12/97	European Patent Office				
	AD							

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
MN	AE	Arvind et al., "Using Term Rewriting Systems to Design and Verify Processors", IEEE Micro Special Issue, May/June 1999
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MN	AO	Subrahmanyam et al., "Ayutomated Synthesis of Mixed-Mode (Asynchronous and Synchronous) Systems AT&T Technical Journal (January 1991)
MN	AP	Windley, P., "Verifying Pipelined Microprocessors", Brigham Young University, 1995
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Examiner Signature	Date Considered
	11/15/04

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